

LOC	DIST	REV	DESCRIPTION	DATE	DWN	APVD
GP	00	A	NEW DRAWING PER RTS 613291.2	29FEB2012	KH	AS

- △ HOUSING: LCP, UL94V0, COLOR: BLACK.
CONTACT: PHOSPHOR BRONZE.
POST: BRASS WIRE
- △ CONTACT: 1.27µm MIN GOLD IN PAD CONTACT AREA
1.27µm MIN TIN-LEAD ON PCB TAIL
OVER 1.27µm MIN NICKEL OVER ALL.
POST 1.27µm MIN NICKEL PLATED.
- △ PLATED THROUGH HOLE REQUIREMENTS
PER 114-13056, FIGURE 4.
- △ SEE TABLE 1 FOR INTERCONNECTIONS TO
BACKPLANE CONNECTOR.

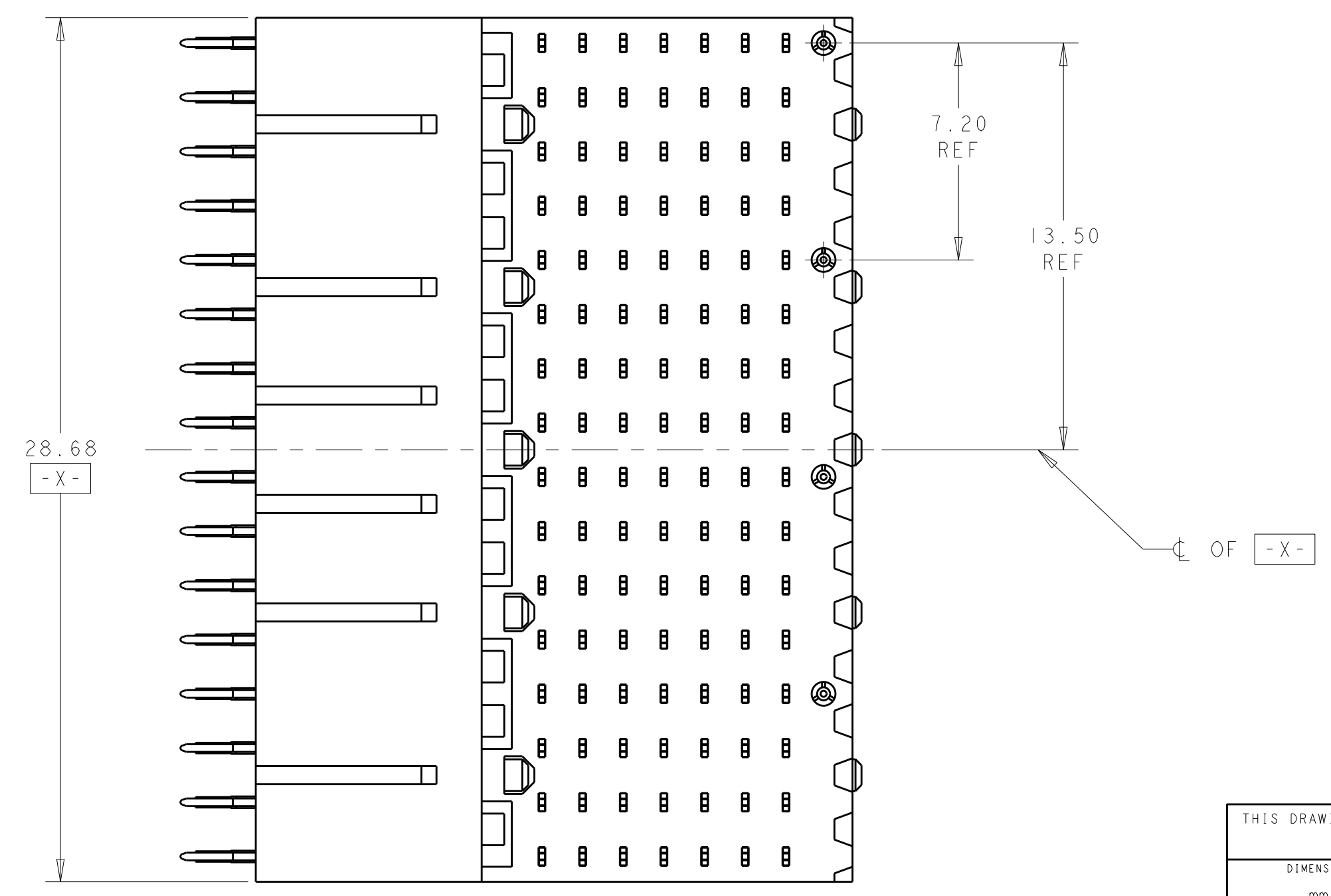
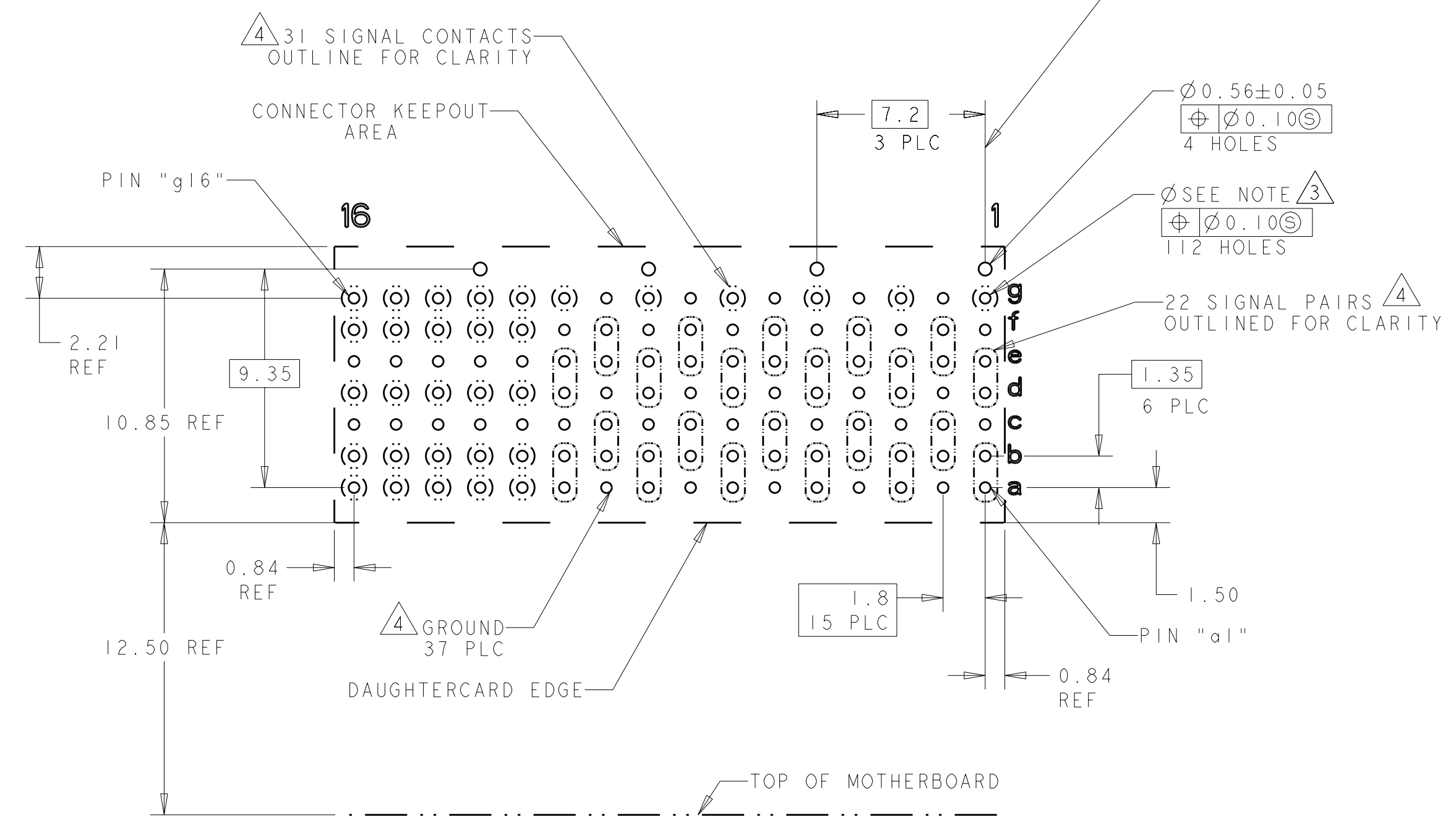
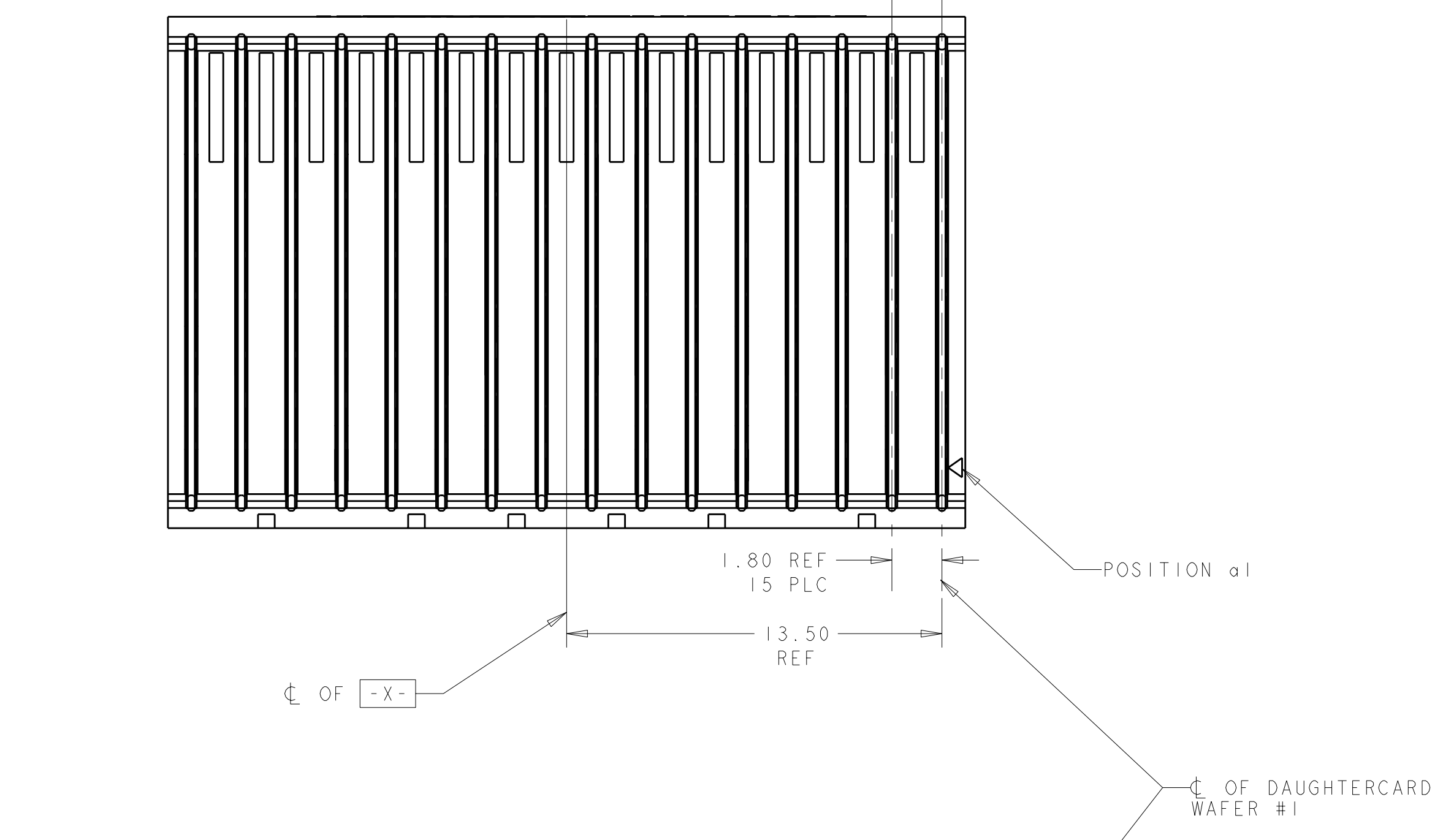
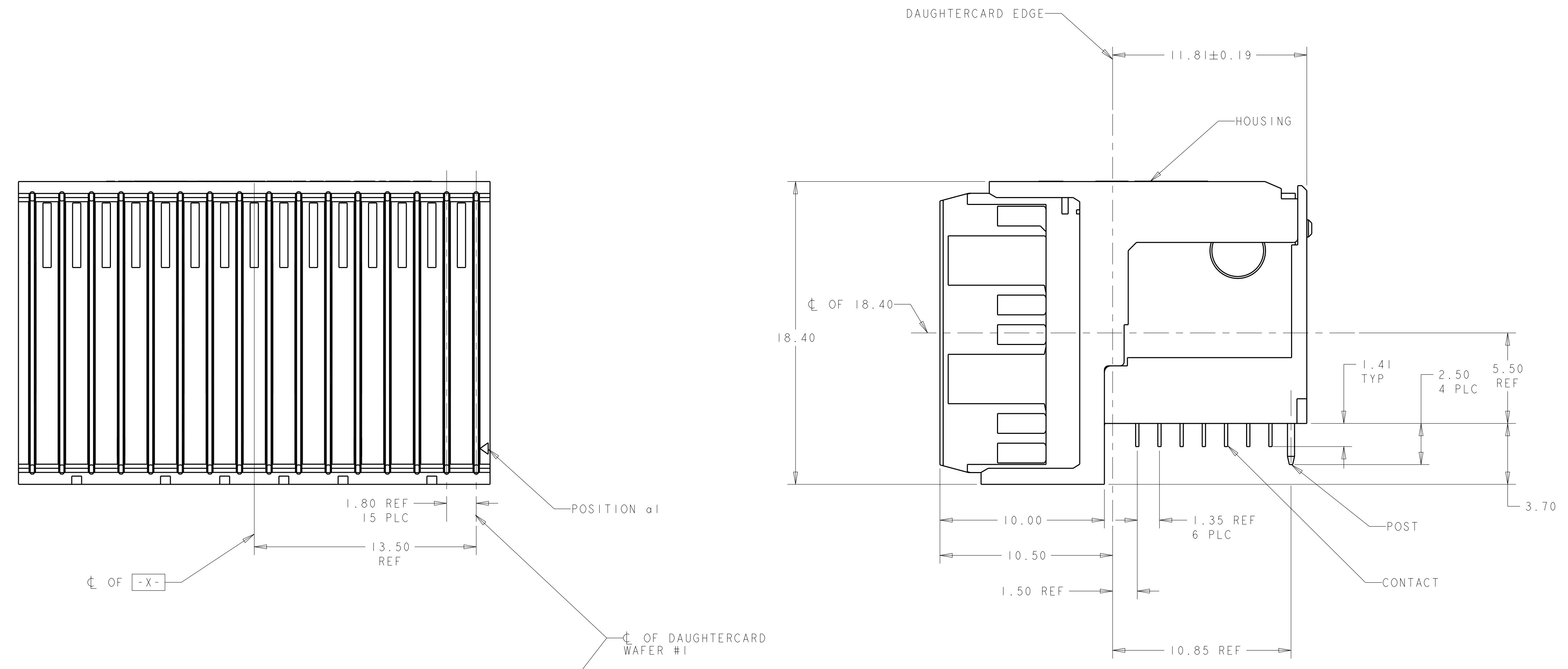
TABLE 1
INTERCONNECTIONS WITH BACKPLANE CONNECTOR 1410140

TYPICAL INTERCONNECTIONS FOR EACH EVEN-NUMBERED COLUMN (WAFER): 2,4,6,8,10		
CONTACT USAGE	DAUGHTERCARD CONNECTOR PIN	BACKPLANE CONNECTOR PIN
SIGNAL PAIR	bx	cx
	cx	dx
SIGNAL PAIR	ex	gx
	fx	hx
GROUPS	ax, dx, gx, (ALL COMMONED)	ax, bx, ex, fx, ix

TYPICAL INTERCONNECTIONS FOR EACH ODD-NUMBERED COLUMN (WAFER): 1,3,5,7,9,11		
CONTACT USAGE	DAUGHTERCARD CONNECTOR PIN	BACKPLANE CONNECTOR PIN
SIGNAL PAIR	ax	ax
	bx	bx
SIGNAL PAIR	dx	ex
	ex	fx
SIGNAL	gx	ix
GROUPS	cx, fx (ALL COMMONED)	cx, dx, gx, hx

TYPICAL INTERCONNECTIONS FOR EACH COLUMN (WAFER): 12,13,14,15,16		
CONTACT USAGE	DAUGHTERCARD CONNECTOR PIN	BACKPLANE CONNECTOR PIN
SIGNAL	ax	bx
SIGNAL	bx	cx
SIGNAL	dx	ex
SIGNAL	fx	gx
SIGNAL	gx	hx
GROUPS	cx, ex, (ALL COMMONED)	ax, dx, fx, ix

NOTE: "x" DESIGNATES THE COLUMN NUMBER



PC BOARD LAYOUT (CONNECTOR SIDE)
SCALE 5:1

THIS DRAWING IS A CONTROLLED DOCUMENT.

DWN: KIRAN HOLL 29FEB2012
CHK: SHARPE ALEXANDER 29FEB2012
APVD: SHARPE ALEXANDER 29FEB2012

TE Connectivity

RIGHT-ANGLE PLUG ASSEMBLY, 7 ROW, CENTER, 20.3mm, MultiGig RT2, DAUGHTERCARD CENTER DIFFERENTIAL PAIR AND SINGLE ENDED

108-2012 APPLICATION SPEC

114-13056

100779C=1410366

SCALE 5:1 SHEET 1 OF 1 REV A